



LOYOLA COLLEGE (AUTONOMOUS), CHENNAI – 600 034

B.A. DEGREE EXAMINATION – ECONOMICS

FIRST SEMESTER – NOVEMBER 2017

17UFR1RL02 - BEGINNERS FRENCH - I

Date: 09-11-2017
Time: 01:00-04:00

Dept. No.

Max. : 100 Marks

PART – A

Answer ALL questions

10 X 2 = 20

1. What is a linker/loader?
2. List the major internal data structure of an assembler.
3. What are relative loaders?
4. Define dynamic linking
5. Write the methods of relocation.
6. What are positional parameters?
7. Define lexical analyzer.
8. What are prologue and epilogue?
9. What are the functions of text editors?
10. Write the use of HELP menu in interactive system.

PART – B

Answer ALL questions

5 X 8 = 40

11. a) Write short note on SIC/XE machine architecture.
Or
b) Illustrate on various assembler design options.
12. a) Describe the basic loader functions.
Or
b) Elucidate on MS-Dos linkers.
13. a) Write note on the following:
(i) Macro definition and expansion **(4 Marks)**
(ii) Macro process algorithm and data structures **(4 Marks)**
Or
b) Discuss on conditional macro expansion and keywords of macros.
14. a) What is syntactic analysis? Explain with example.
Or
b) Write note on machine – dependent features of compilers.
15. a) Write short note on overview of edition process and features of user interface.
Or
b) Illustrate on interactive debugging system.

PART – C

Answer any TWO questions

2 X 20 =40

16. a) Explain machine- independent assembler features.
 - b) Elaborate on design options of loaders.
17. a) Explain the basics of macro processing features.
 - b) What are compilers? Discuss on its design options.
18. a) Explain the methodology and functions of text editors with diagram.
 - b) “Java compiler is optimally designed compiler” – Explain with